



Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Page 1 of 5	Application No.	10/613,891
	Filed	7/2/2003
	First Inventor	Srinivasan, Varadarajan
	Art Unit	2867 2616
	Examiner	Betty Lee
	Atty. Docket No.	NLMI.P194

Non Patent Literature Documents		
Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Trans- lation
/BL/	Advanced Traffic Management for Multiservice ATM Networks, www.net.com, Pub. Dec. 15, 2000, (12/15/00), 22 pgs.	
/BL/	Agere Ads Additional PaloadPlus Processor to Product Line, Agere Press Release, November 29, 2000 (11/29/00), 3 pgs.	
/BL/	Architecture and Design of Function Specific Wire-Speed Routers for Optical Internetworking, published by Entridia Corp., December 6, 2000 (12/6/00), 60 pgs.	
/BL/	ATLAS I: A General-Purpose, Single-Chip ATM Switch with Credit-Based Flow Control, IEEE Hot Interconnects IV Symposium Proceedings, Stanford, CA, Pub. August 15-17, 1996, 11 pgs.	
/BL/	CSIX-L1: Common Switch Interface Specification-L1, published by CSIX, August 5, 2000(8/5/2000), 72 pgs.	

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/BL/	genFlow CAN-2500gF OC48c Multiprotocol Traffic Management Coprocessor, Acorn Networks, Pub. Date Unknown, 4 pgs.	
/BL/	genFlow OC-48c Multiprotocol Traffic Management Coprocessor, Acorn Networks, Pub. Date unknown, 4 pgs.	
/BL/	Hierarchical Packet Fair Queueing Algorithms, Jon C.R. Bennett, Hui Zhang, Pub. Date Unknown, 14 pgs.	
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/BL/	PayloadPlus Routing Switch Processor, Lucent Technologies, April 2000, 6 pgs.	
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/BL/	Smart Memory Technology in the MAN, Silicon Access Networks, Pub. October 2000, 6 pgs	
/BL/	Smart Memory Technology Target Markets, Silicon Access Networks, Pub. October 2000, 5 pgs.	
/BL/	Start-time Queuing: A Scheduling Algorithm for Integrated Services Packet Switching Networks, Pawan Goyal, Harrick M. Vin and Haichen Cheng, Distributed Multimedia Computing Laboratory, Dept. Computer Sciences, University of Texas at Austin, Pub. 1996, 12 pgs.	

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	Art Unit	2661 2616
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/BL/	Traffic Scheduling in Packet-Switched Networks: Analysis, Design and Implementation, university of California, Santa Cruz, Pub. June 1996 (6/1996), 107 pgs.	
/BL/	Traffic Stream Processor MXT 4400, Conexant, Pub. Date Unknown, 5 pgs.	
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/BL/	Wire Speed Quality of Service Over Ethernet, Switchcore, Pub. May 8, 2000 (5/8/00), 19 pgs.	

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/BL/	Zettacom: Hurry Up and Wait, www.lightreading.com, Pub. Nov. 29, 2000 (11/29/00), 2 pgs.	

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U.S. Patent Documents							
Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication	Relevant Pages, Columns, Lines	CLASS	SUBCLASS
	Number	Kind					
/BL/	2003/0016686		Wynne et al.	1/1/2003		—	—
/BL/	2006/0007871		Wellin	1/1/2006		—	—
/BL/	5,875,173		Ohgane et al.	2/23/1999		—	—
/BL/	6,975,629	B2	Wellin	12/13/2005		—	—

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/BL/	Blake et al, "An Architecture for Differentiated Services," RFC 2475, December, 1998.	
/BL/	Heinanen et al, "A Single Rate Three Color Marker," RFC 2697, September 1999.	

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